



MANIPAL ACADEMY OF HIGHER EDUCATION

B.Tech 1st Semester Midterm Examination September 2023

BASIC ELECTRONICS [ECE 1071]

Marks: 30

Duration: 120 mins.

MCQ

Answer all the questions.

Section Duration: 20 mins

1) An Op-amp as a voltage follower has a voltage gain of _____

- 1) Infinity 2) Zero 3) Less than unity 4) Unity

(0.5)

Correct option is: 4

2) If the differential voltage gain and common mode gain of the differential amplifier are 48dB and 2dB, respectively, then the common mode rejection ratio is _____

- 1) 23 dB 2) 25 dB 3) 46 dB 4) 50 dB

(0.5)

Correct option is: 3

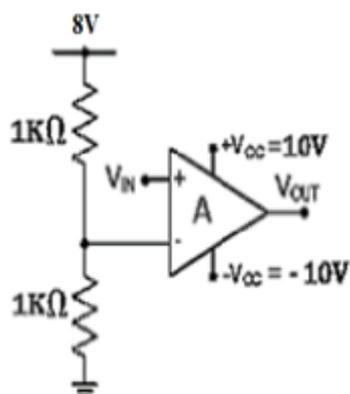
3) Output of a certain circuit is applied as input to the OPAMP-based inverting amplifier. If the amplifier output voltage desired is +10V, then the input voltage applied to the amplifier must be _____. Assume that $\pm V_{CC} = 15V$, $R_F = 10\text{ k}\Omega$ and $R_1 = 5\text{ k}\Omega$.

- | | | | |
|-------|--------|-------|--------|
| 1) 1V | 2) -1V | 3) 5V | 4) -5V |
|-------|--------|-------|--------|

(0.5)

Correct option is: 4

4) For the comparator circuit shown in the figure, if the input voltage V_{IN} is 3V, the output voltage V_{OUT} is _____



(0.5)

- | | | | |
|---------|---------|--------|--------|
| 1) +10V | 2) -10V | 3) +3V | 4) -8V |
|---------|---------|--------|--------|

Correct option is: 2

5) Which of the following is not the ideal characteristic of Op-amp?

(0.5)

1) Infinite open-loop gain

2) Infinite CMRR

3) Infinite bandwidth

4) Infinite output resistance

Correct option is: 4

6) The threshold voltage of an n channel MOSFET is 0.5V. When the device is biased at a gate voltage of 4V, pinch-off would occur at a drain voltage of _____

- 1) 2.5 V 2) 4 V 3) 0.5V 4) 3.5 V

(0.5)

Correct option is: 4

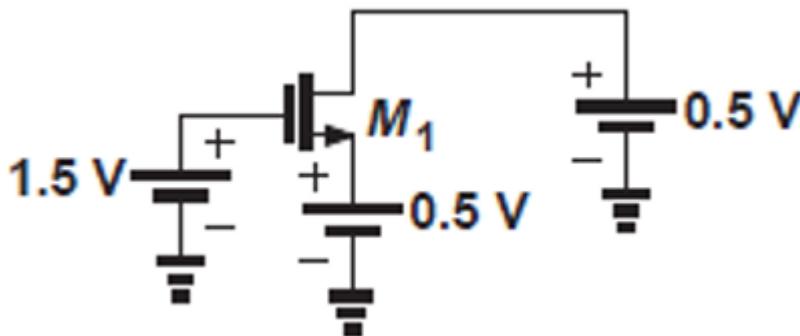
7) When drain voltage equals the pinch-off-voltage, drain current _____ with the increase in drain voltage. Assume that $\lambda=0$

- 1) decrease 2) increase 3) remains constant 4) none of the above

(0.5)

Correct option is: 3

8) The M1 in the given circuit is operating in _____ region, if its $V_{TH} = 0.6 V$?



(0.5)

1) Linear Region

2) Saturation Region

3) Cut-off Region

4) Edge of saturation region

Correct option is: 1

9) Find the PIV rating of the diode used for proper working of a full-wave bridge rectifier when it is supplied with 230V, 50Hz AC mains through a step-down transformer with turns ratio =10:1

- 1) 32.52 V 2) 46 V 3) 12.5 V 4) 0 V

(0.5)

Correct option is: 1

10) A sinusoidal secondary voltage of peak value of 10 V and frequency 50 Hz is applied to HWR. If the load resistance is 800Ω. Assume ideal diode. Calculate average load voltage.

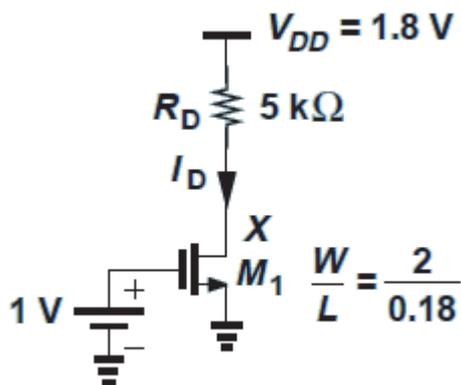
- | | | | |
|-----------|-----------|-----------|-----------|
| 1) 3.18 V | 2) 3.96 V | 3) 2.18 V | 4) 2.96 V |
|-----------|-----------|-----------|-----------|

(0.5)

Correct option is: 1

Answer all the questions.

- 11) Derive the expression for drain current in an n-channel MOSFET. Draw and explain different regions of operation. (4)
- 12) Explain the operation of centre-tap type FWR with neat circuit and waveform. Derive the expressions for V_{dc} , V_{rms} and efficiency. (4)
- 13) In a Zener network, $R_S = 120\Omega$, $R_L = 250\Omega$, $V_Z = 5V$. Calculate the minimum and maximum Zener current when input voltage is varied between 9V to 15V. (3)
- 14) Calculate the bias current of M_1 . Assume $\mu_n C_{ox} = 100 \mu A/V^2$ and $V_{TH} = 0.4 V$. What choice of R_D places the transistor at the edge of the triode region. (3)

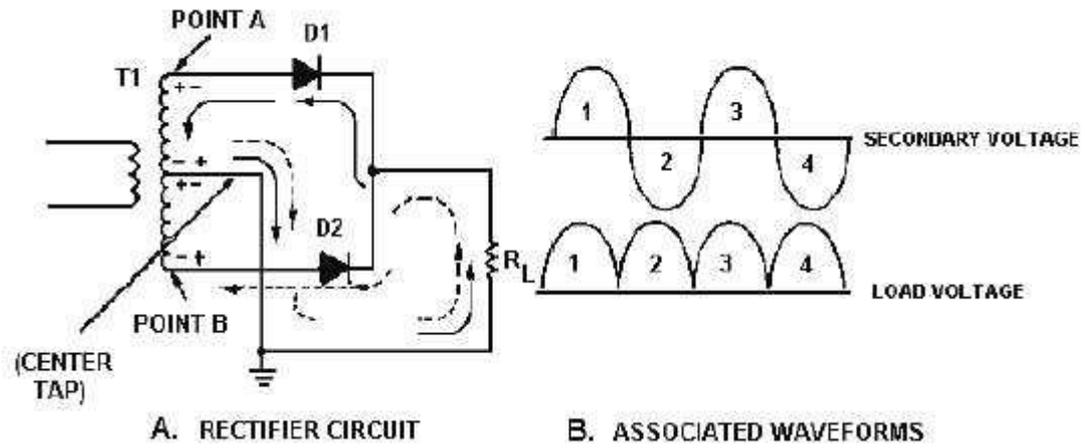


- 15) A germanium diode carries a current of 10mA when a forward bias of 0.2V is applied at room temperature 27°C .
 (a) Estimate the reverse saturation current
 (b) Calculate bias voltage needed for a diode current of 100mA.
 (c) Estimate reverse saturation current at 20°C above room temperature. (3)
- 16) Define CMRR. Calculate CMRR for an OPAMP, when V_1 is 0.25 mV, and V_2 is -0.25 mV, output voltage is 4V. For the same op-amp, when $V_1 = V_2 = 0.5$ mV, the output voltage is 6 mV. (3)
- 17) Design the circuit using two OPAMPs to obtain: $V_0 = 3V_1 - 6V_2 + 9V_3$. Choose $R_f = 10\text{K}\Omega$. (V_1 , V_2 and V_3 are the inputs) (3)
- 18) Briefly explain the working of: (i) Photodiode (ii) LED (2)

-----End-----

Q.12. Explain the operation of centre tap type FWR with neat circuit and waveform. Derive the equation for V_{dc} , V_{rms} and efficiency. (4 Marks)

Answer:



2M

$$V_{dc} = \frac{1}{\pi} \int_0^{\pi} V_m \sin \omega t d(\omega t) = \frac{V_m}{\pi} [-\cos \omega t]_0^{\pi} = \frac{2V_m}{\pi}$$

0.5M

$$V_{rms} = \left[\frac{1}{\pi} \int_0^{\pi} V_m^2 \sin^2 \omega t d(\omega t) \right]^{\frac{1}{2}} = \frac{V_m}{\sqrt{2}}$$

0.5M

$$\eta = \frac{dc \text{ output power}}{ac \text{ input power}} = \frac{P_{dc}}{P_{ac}}$$

$$\frac{V_{dc}^2 / R_L}{V_{rms}^2 / R_L} = \frac{\left[\frac{2V_m}{\pi} \right]^2}{\left[\frac{V_m}{\sqrt{2}} \right]^2} = \frac{8}{\pi^2} = 0.812 = \underline{\underline{81.2\%}}$$

1M

Q.15. A germanium diode carries a current of 10mA when a forward bias of 0.2V is applied at room temperature 27°C.

- (a) Estimate the reverse saturation current
- (b) Calculate bias voltage needed for a diode current of 100mA.
- (c) Estimate reverse saturation current at 20°C above room temperature.

(3 Marks)

Answer:

The diode current $I_D=10\text{mA}$,
 Temperature $T = 273+27 = 300\text{K}$
 The diode is Germanium $\eta=1$
 Forward bias voltage $V_D=0.2\text{V}$
 The equation for the diode current I_D is given by

$$I_D = I_0 \left(e^{\frac{V_D}{\eta V_T}} - 1 \right) \text{ and } V_T = \frac{T}{11600} = \frac{300}{11600}$$

(a) $I_0 = 4.38\mu\text{A}$ 1M

(b) $V_D = 0.25 \text{ V}$ when $I_D = 100 \text{ mA}$ 1M

(c) $I_{02} = 1.75 * 10^{-5} \text{ A}$, given $t_1 = 27^\circ\text{C}$ & $t_2 = 47^\circ\text{C}$ 1M

Q13. In a Zener network, $R_S = 120\Omega$, $R_L = 250\Omega$, $V_Z = 5\text{V}$. Calculate the minimum and maximum Zener current when input voltage is varied between 9V to 15V. (3)

$$I_L = \frac{V_L}{R_L} = \frac{V_Z}{R_L} = 20\text{mA} \quad 1\text{M}$$

$$I_{RS} = \frac{V_i - V_Z}{R_S}$$

$$I_{RS \text{ max}} = \frac{V_{i \text{ max}} - V_Z}{R_S} = 83.33\text{mA} \quad 0.5\text{M}$$

$$I_{RS \text{ min}} = \frac{V_{i \text{ min}} - V_Z}{R_S} = 33.33\text{mA} \quad 0.5\text{M}$$

$$I_{RS \text{ max}} = I_{Z \text{ max}} + I_L$$

$$I_{Z \text{ max}} = I_{RS \text{ max}} - I_L = 63.33\text{mA} \quad 0.5\text{M}$$

$$I_{RS \text{ min}} = I_{Z \text{ min}} + I_L$$

$$I_{Z \text{ min}} = I_{RS \text{ min}} - I_L = 13.33\text{mA} \quad 0.5\text{M}$$

Q 11.

Derive the expression for drain current -

$$I = Q \cdot v \quad (1) \quad v = \mu \frac{dv}{dx} \quad (a)$$

$$Q_{ch} = W C_{ox} (V_{GS} - V_{th} - v(x)) \quad (b)$$

Using (a) & (b) in eqn (1)

$$I_D = W C_{ox} [V_{GS} - V_{th} - v(x)] \mu_n \frac{dv(x)}{dx}$$

$$\int_0^L I_D \cdot dx = \mu_n C_{ox} \cdot W \int_0^{V_{DS}} (V_{GS} - V_{th} - v(x)) dv$$

$$I_D = \mu_n C_{ox} \cdot \frac{W}{L} \left[(V_{GS} - V_{th}) V_{DS} - \frac{1}{2} V_{DS}^2 \right]$$

is drain current.

MOSFET is in triode region.

Case 1: If $V_{DS} \ll 2(V_{GS} - V_{th})$

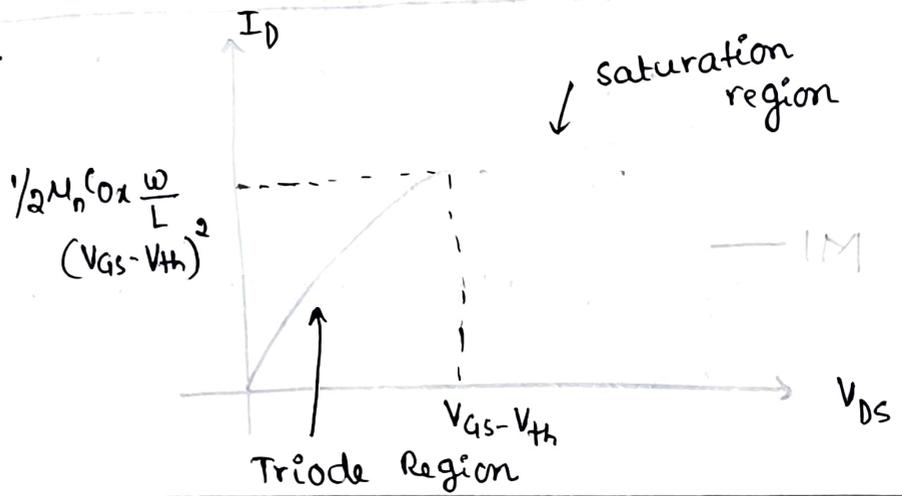
$$\text{then } I_D \approx \mu_n C_{ox} \cdot \frac{W}{L} (V_{GS} - V_{th}) V_{DS}$$

MOSFET in Deep triode region.

Case 2: If $V_{DS} > V_{GS} - V_{th}$, channel is pinched off.

$$\therefore I_D = \frac{1}{2} \mu_n C_{ox} \cdot \frac{W}{L} (V_{GS} - V_{th})^2$$

Saturation region.

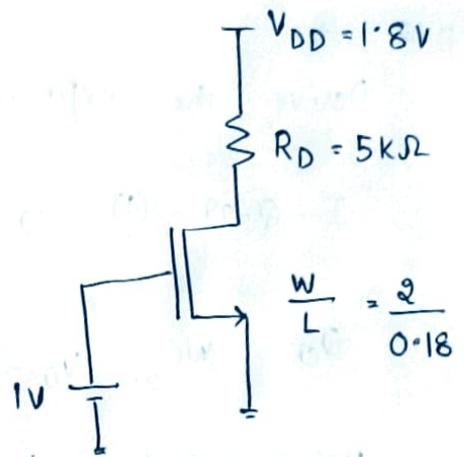


Q14)

Given:

$$\mu_n C_{ox} = 100 \mu\text{A}/\text{V}^2$$

$$V_{th} = 0.4 \text{ V}$$



Assuming M_1 operates in saturation,

$$\begin{aligned} I_D &= \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{th})^2 \\ &= \frac{1}{2} \times 100 \times 10^{-6} \times \frac{2}{0.18} (1 - 0.4)^2 \\ &= \underline{\underline{200 \mu\text{A}}} \end{aligned}$$

$$\begin{aligned} V_{DS} &= V_{DD} - I_D R_D = 1.8 - 200 \times 10^{-6} \times 5 \times 10^3 \\ &= \underline{\underline{0.8}} \end{aligned}$$

$V_{DS} > (V_{GS} - V_{th})$. Hence MOSFET operates in saturation region.

For edge of triode region:

$$V_{DS} = V_{GS} - V_{th} = 0.6 \text{ V}$$

$$\text{ie } V_{DS} = V_{DD} - I_D R_D$$

$$0.6 = 1.8 - 200 \times 10^{-6} \times R_D$$

$$R_D = \underline{\underline{6 \text{ k}\Omega}}$$

$R_D = 6 \text{ k}\Omega$ places MOSFET at the edge of triode region.

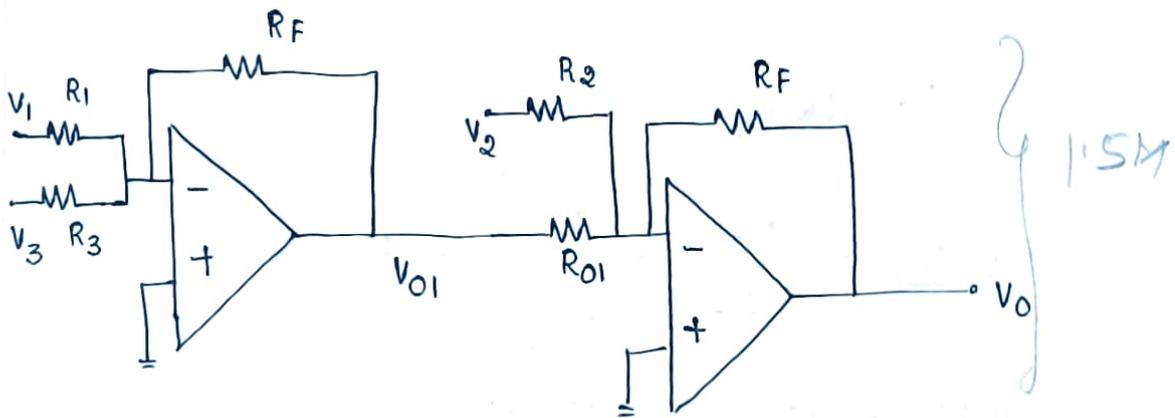
Q17) $V_0 = 3V_1 - 6V_2 + 9V_3$, V_1, V_2 & V_3 are inputs
Use 2 - opamps.

$$V_0 = -6V_2 - (- (3V_1 + 9V_3))$$

$$= -6V_2 - V_{01} \quad , \text{ where } V_{01} = -(3V_1 + 9V_3)$$

Realize V_{01} using op-amp adder and

$V_0 = -6V_2 - V_{01}$ using another adder.



Choose $R_F = 10k\Omega$,

$$V_{01} = -\left(\frac{R_F}{R_1} V_1 + \frac{R_F}{R_3} V_3\right) = -(3V_1 + 9V_3)$$

$$\therefore R_1 = \frac{R_F}{3} = \frac{10}{3} k\Omega = \underline{\underline{3.33 k\Omega}}$$

$$R_3 = \frac{R_F}{9} = \frac{10}{9} k\Omega = \underline{\underline{1.11 k\Omega}}$$

$$V_0 = -6V_2 - V_{01} = -\left(\frac{R_F}{R_2} V_2 + \frac{R_F}{R_{01}} V_{01}\right)$$

$$\therefore R_2 = \frac{R_F}{6} = \frac{10}{6} k\Omega = 1.67 k\Omega \quad , \quad R_{01} = R_F = 10k\Omega$$

Q 16)

Definition of CMRR. — 1M

Output voltage of opamp $V_o = A_d V_d + A_c V_c$ when $V_1 = 0.25 \text{ mV}$ & $V_2 = -0.25 \text{ mV}$, o/p voltage = 4V

$$4 = A_d \times 0.5 \times 10^{-3} + A_c(0)$$

$$\underline{A_d = 8 \times 10^3}$$

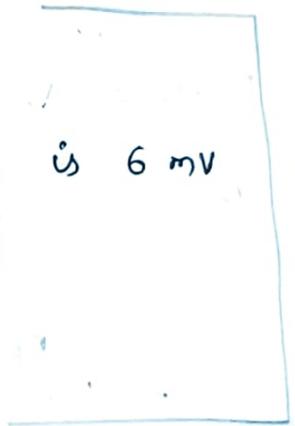
when $V_1 = V_2 = 0.5 \text{ mV}$ o/p voltage is 6 mV

$$6 \times 10^{-3} = A_d(0) + A_c \times 0.5 \times 10^{-3}$$

$$\underline{A_c = 12}$$

$$\text{CMRR} = \frac{A_d}{A_c} = \frac{8 \times 10^3}{12} = \underline{666.67}$$

$$\text{CMRR in dB} = \underline{56.47 \text{ dB}}$$



1.5M

— 1/2 M